

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	BIPOLAR JUNCTION TRANSISTOR AND FABRICATING METHOD
<p>Application Number :</p> <p>Date :</p> <p>First Named Applicant: Anchor Chen</p> <p>Confirmation Number:</p> <p>Attorney Docket Number: NAUP0486USA3</p>	
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<p>Submitted By:</p> <p>Winston Hsu Registered Number: 41,526</p>	<p>Elec. Sign.</p> <p>Winston Hsu</p>

Documents being submitted:	Files
us-request	NAUP0486-3-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	NAUP0486-3-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	NAUP0486-3ASS1.tif
application-body	NAUP0486-3-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd Naup0486usa-01.TIF Naup0486usa-02.TIF Naup0486usa-03.TIF Naup0486usa-04.TIF Naup0486usa-05.TIF Naup0486usa-06.TIF Naup0486usa-07.TIF Naup0486usa-08.TIF Naup0486usa-09.TIF Naup0486usa-10.TIF Naup0486usa-11.TIF Naup0486usa-12.TIF Naup0486usa-13.tif Naup0486usa-14.tif

us-declaration	NAUP0486-3DEC1.tif
us-declaration	NAUP0486-3DEC2.tif
Comments	